Course Title: Logic Circuits
Course Code: ECE 224C
Instructor: Dr. Islam ElShaarawy
Other Resources:
http://www.cburch.com/logisim/
http://bbrown.spsu.edu/digitalworks/
http://bleyer.org/icarus/
http://vol.verilog.com/

Course Contents:
1. Digital Systems and Binary Numbers
2. Boolean Algebra and Logic Gates
3. Gate-Level Minimization
4. Combinational Logic
5. Synchronous Sequential Logic
6. Registers and Counters

Attendance Policy:
1. Attendance is expected.
2. Whether you attend or not, you are responsible for keeping track of whatever happens during the lectures and the tutorials.
3. Side talking, cellphones, laptops, food/drinks, and walking are not allowed.

Grading System:

<table>
<thead>
<tr>
<th>Component</th>
<th>Percentage</th>
</tr>
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<tbody>
<tr>
<td>Attendance¹</td>
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<tr>
<td>Assignments</td>
<td>10</td>
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<tr>
<td>Quizzes</td>
<td>10</td>
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<tr>
<td>Midterm Exam</td>
<td>10</td>
</tr>
<tr>
<td>Oral Exam²</td>
<td>30</td>
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<tr>
<td>Final Exam</td>
<td>90</td>
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</tbody>
</table>

¹ You can use an earlier edition if you already have one.
² If you are late for the lecture, then you are allowed to walk into the lecture hall but quietly.
³ Attendance will be taken anyway.
⁴ There will be a semester project that will be discussed during the oral exam.